

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): D. Bhattacharya et al.  
Case: 3-5-8-1-25  
Serial No.: To Be Assigned  
Filing Date: January 30, 2004  
Group: To Be Assigned  
Examiner: To Be Assigned

Title: Improved Semiconductor Resistor

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**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicants' attorney wishes to bring to the attention of the Patent and Trademark Office the following documents listed on the accompanying Form PTO-1449.

**U.S. Patent Documents**

1. U.S. Patent No. 5,977,897 issued on 11/02/99 to Barnes et al.
2. U.S. Patent No. 5,534,862 issued on 07/09/96 to Gross, Jr. et al.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability, or as a representation that no other material information exists.

Respectfully submitted,



Date: January 30, 2004

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**FORM PTO-1449 (MODIFIED)**

Applicant(s): D. Bhattacharya et al.  
Case: 3-5-8-1-25  
Serial No.: TBA  
Filing Date: January 30, 2004  
Group: TBA

**LIST OF PUBLICATIONS FOR  
APPLICANT'S INFORMATION  
DISCLOSURE STATEMENT**

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**U.S. PATENT DOCUMENTS**

EXAMINER				FILING DATE
INITIAL	DOCUMENT NO.	DATE	NAME	IF APPROPRIATE
____	1. 5,977,897	11/02/99	Barnes et al.	
____	2. 5,534,862	07/09/96	Gross, Jr. et al.	

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**FOREIGN PATENT DOCUMENTS**

EXAMINER				TRANSLATION	
INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	YES NO
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**OTHER DOCUMENTS**

EXAMINER		
INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
____		

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Examiner

Date Considered

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**Examiner:** Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.